

111 Amendment
09/867,124 (09792909-5028)
Page 7

REMARKS

Claims 1 and 6 are amended and claims 4, 13, and 18 are canceled herein. Upon entry of this amendment, claims 1-3, 5-12, 14-17, and 19 will be pending.

Section 102

Applicant respectfully requests reconsideration of the rejection of claims 1-3, 5-8, 10-12, 14-17, and 19 under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,369,789 (Ulrich).

Claims 1-3 and 5 recite a liquid crystal display drive method, said liquid crystal display comprising a first electrode and a second electrode, wherein pictures are displayed by means of a voltage signal impressed between said first and second electrodes, wherein a drive voltage waveform comprising a display signal period and a control signal period irrelevant to display is used within one frame, wherein the drive voltage waveform comprises a plurality of bit planes and the control signal period occurs during a preselected number of successive bit planes within the plurality of bit planes, and wherein the ratio of the control signal period to the whole drive voltage waveform period is between about five percent and about fifty percent.

Claims 6-8, 10-12, 14-17, and 19 recite a liquid crystal display method, said liquid crystal display comprising a first electrode, and a second electrode, wherein pictures are displayed by means of a voltage signal impressed between said first and second electrodes to select at least one state of incident light, wherein a drive voltage waveform comprising a display signal period and a control signal period irrelevant to display is used within one frame, wherein the drive voltage waveform comprises a plurality of bit planes and the control signal period occurs during a preselected number of successive bit planes within the plurality of bit planes, wherein the ratio of said control signal period to the whole drive voltage waveform period is between about five percent and about fifty percent, and wherein one of the two states, either on or off, is chosen to select the state of incident light in said display signal period.

Ulrich discloses a method of reducing the effects of ionic memory in an FLC material to which a switching pulse is applied comprising the steps of adding ionic

111 Amendment
09/867,124 (09792909-5028)
Page 8

dopant to the FLC material, and following the switching pulse with a TRIFLE pulse of opposite polarity. However, Ulrich does not disclose a liquid crystal display method in which a drive voltage waveform comprising a display signal period and a control signal period irrelevant to display is used within one frame, the drive voltage waveform comprises a plurality of bit planes and the control signal period occurs during a preselected number of successive bit planes within the plurality bit planes, and a ratio of the control signal period to the whole drive voltage waveform period is between about five percent and about fifty percent. Because every element recited in the claims is not found in a single reference, the Section 102 rejection is improper and should be withdrawn. Accordingly, Applicant respectfully requests the Section 102 rejection be withdrawn.

Section 103

Applicant respectfully requests reconsideration of the rejection of claim 9 under 35 U.S.C. § 103(a) as being unpatentable over Ulrich in view of U.S. Patent No. 6,417,828 (Sato) and U.S. Patent No. 6,057,878 (Ogiwara).

Claim 9 recites a liquid crystal display drive method, said liquid crystal display comprising a first electrode, and a second electrode, wherein pictures are displayed by means of a voltage signal impressed between said first and second electrodes to select at least one state of incident light, wherein a drive voltage waveform comprising a display signal period and a control system period irrelevant to display is used within one frame, wherein the drive voltage waveform comprises a plurality of bit planes and the control signal period occurs during a preselected number of successive bit planes within the plurality of bit planes, wherein the ratio of said control signal period to the whole drive voltage waveform period is between about five percent and about fifty percent, and wherein one of the two states, either on or off, is chosen to select the state of incident light in said display signal period.

As discussed above, Ulrich fails to disclose a liquid crystal display method in which a drive voltage waveform comprising a display signal period and a control signal period irrelevant to display is used within one frame, the drive voltage waveform

23137427V-1

111 Amendment
09/867,124 (09792909-5028)
Page 9

comprises a plurality of bit planes and the control signal period occurs during a preselected number of successive bit planes within the plurality of bit planes, and a ratio of the control signal period to the whole drive voltage waveform period is between about five percent and about fifty percent. The secondary references, considered alone or in combination with each other and Ulrich, also fail to disclose or suggest these recited features. Accordingly, the Section 103 rejection is improper and should be withdrawn.

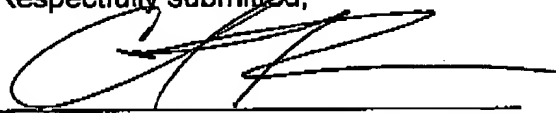
Conclusion

If the Examiner believes that there is an issue that could be resolved by an interview, Applicant requests the Examiner contact the undersigned attorney at the telephone number listed below.

As it is believed the application is in condition for allowance, a favorable action and Notice of Allowance are respectfully requested.

July 21, 2004

Respectfully submitted,



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